

HP 8590 Series Analyzers

Component-Level Information

HP Part Number 5963-2951

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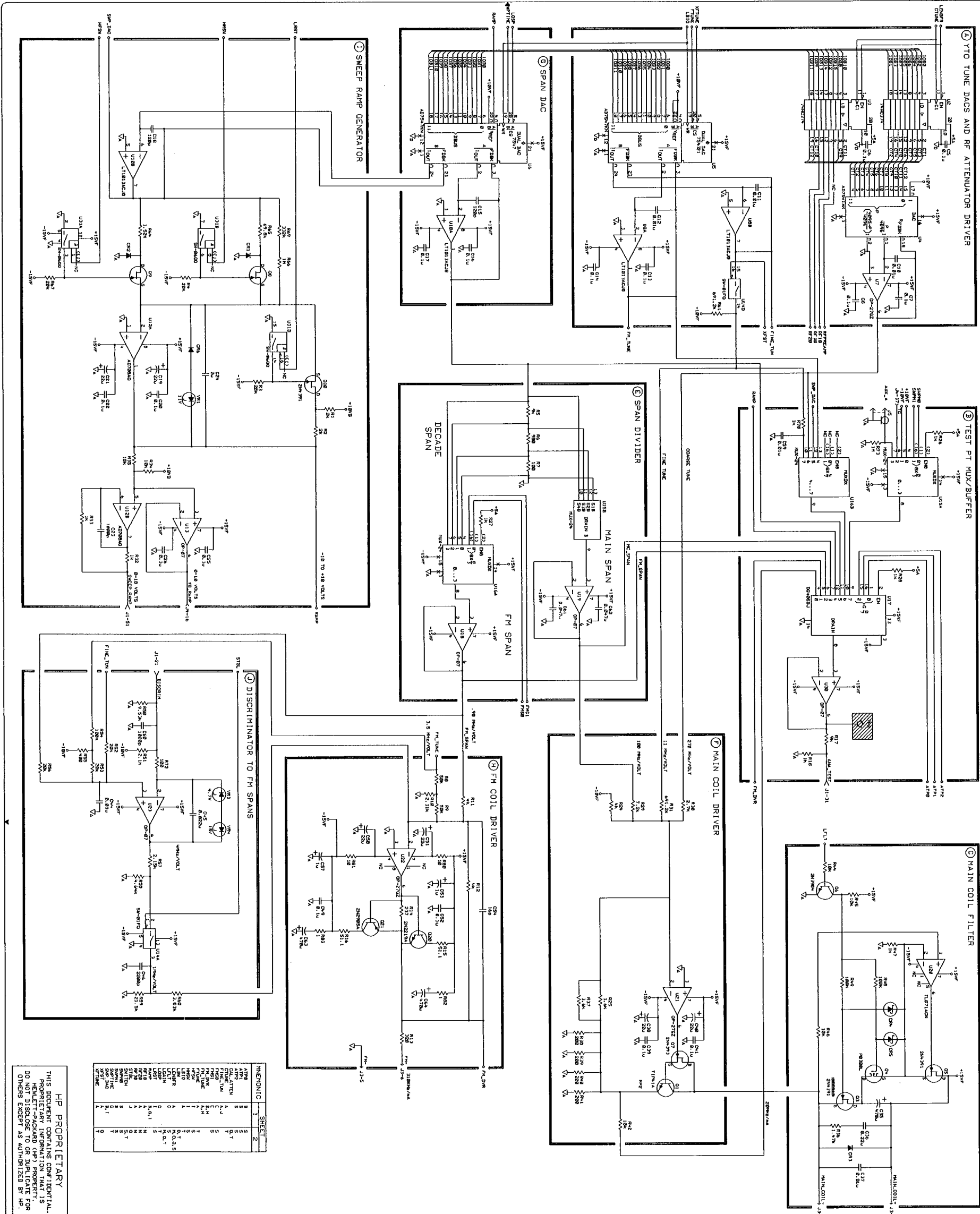
Reference Designator	Description	HP Part Number
N/A	General CLIP Information	5958-7060
A1A1	Keyboard	08590-60201
A3A1A1	Comb Generator	08569-60026
A5A1	Second Converter Tripler	08590-60155
A7	Analog Interface	08590-60197
A7	Analog Interface	08591-60072
A7	Analog Interface	08592-60108
A7	Analog Interface	08594-60030
A7	Analog Interface	08595-60022
A7A1	Tracking Generator Control	08590-60196
A9	3RD Converter	08590-60344
A9	3RD Converter	08591-60081
A9	3RD Converter	08593-60062
A10	Tracking Generator Control	5063-0635
A10	L.O.D.A. Control	5062-8232
A11/A13	Bandwidth Filter	08590-60407
A12	Amplitude Control	08590-60105
A14	Log Amplifier	08590-60386
A15	Motherboard	08590-60218
A15	Motherboard	08591-60062
A16	Processor/Video	08590-60365
A16A1	2X Memory	08590-60399
A17	Memory Card Reader	08590-60396
A22	10 MHz TCXO	08591-60048
A25	Counterlock	08591-60098
A41/A43	Dual I/O	08590-60366
A101	Fast ADC	5062-7079
A102	Demod/TV SYNC	5062-1982
A103	Demod/QPD	5062-1983
A103	Demod/QPD	85462-60042
A110	Cellular Demod/TV	5063-0234
A180	TV Display	08590-60374

Component-Level Information Packet

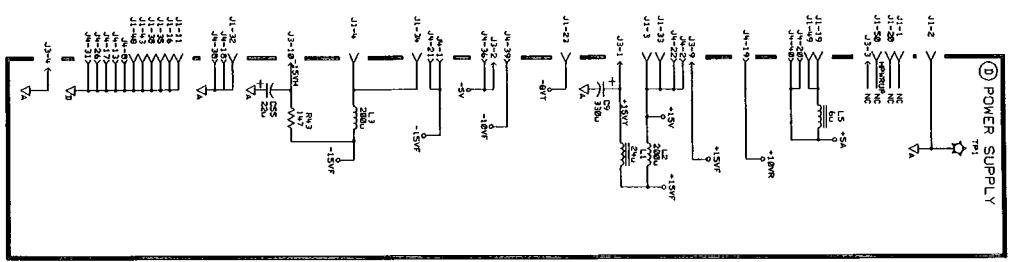
General CLIP Information



HP Part No. 5958-7060
Printed in USA November 1992



FUNCTION	SHEET
YTO TUNE DACS AND RF ATTENUATOR DRIVER	1
TEST PT MUX/BUFFER	2
MAIN COIL FILTER	3
MAIN COIL DRIVER	4
SPAN DIVIDER	5
FM COIL DRIVER	6
DISCRIMINATOR TO FM SPANS	7
SNEEP RAMP GENERATOR	8
SPAN DAC	9
YIG	10



CONNECTOR	FUNCTION
J1	MOTHERBOARD
J2	2ND CONV & ATTEN.
J3	YIG
J4	TG

CONNECTOR	FUNCTION
J5	SPAN DAC
J6	FM COIL DRIVER
J7	DISCRIMINATOR TO FM SPANS
J8	SNEEP RAMP GENERATOR
J9	SPAN DAC
J10	YIG

NOTES:
 1. REFERRED DESIGNATORS WITHIN THIS SCHEMATIC ARE ASSIGNED AND NUMBERED FOR COMPLETE REFERENCE TO THE CORRESPONDING PARTS LIST.
 2. RESISTORS AND CAPACITORS ARE INDICATED BY VALUE IN THE SCHEMATIC UNLESS OTHERWISE SPECIFIED.
 3. INDUCTIVE TABLE.

MATCHED COMPONENTS ARE NOT LOADED FROM THIS BOARD.
 PART NUMBER: 08591-60072-1

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DESIGN BY:	DATE:	SCHEMATIC, PWA
ENGINEER/CHECKER:	DATE:	ANALOG
RELEASE TO PROD:	DATE:	INTERFACE BOARD
SUPERSIDES DWG:	DATE:	